Bahria University,

Karachi Campus

A picture containing text, room

Description automatically generated

LAB EXPERIMENT NO:

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13

LIST OF TASKS

|  |  |
| --- | --- |
| TASK NO | OBJECTIVE |
|  |  |
| 01 | Construct the circuits of the following using multiplexer on Multisim along with the truth tables:   1. 4 variable expressions:   F(A,B,C,D)= Σ (1,3,4,11,12,13,14,15)  F(A,B,C,D)= π (1,3,4,11,12,13,14,15)   1. full adder design 2. 3) basic ALU Design (AND, OR, NAND, NOR) |
|  |  |

Submitted On:

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/01/24

(Date: DD/MM/YY)